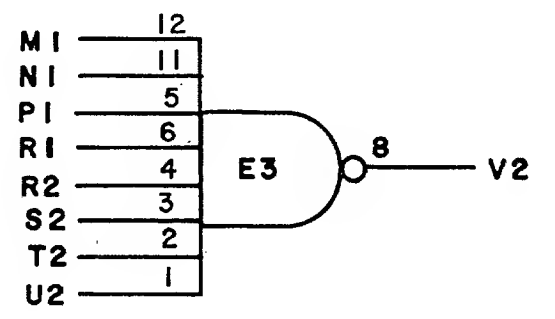
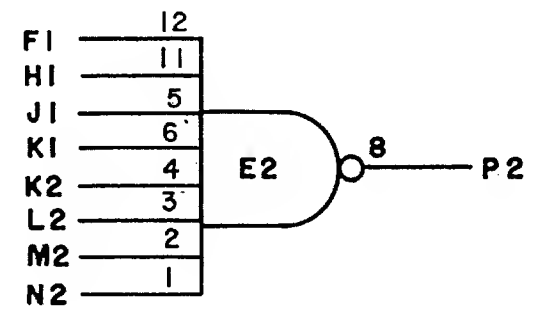
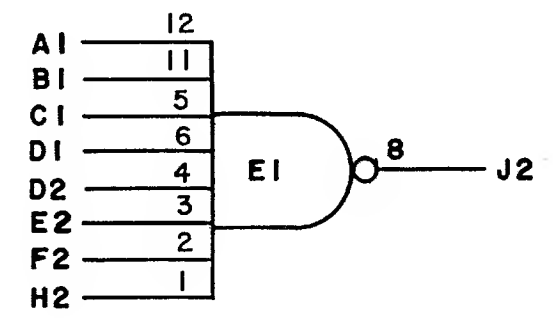
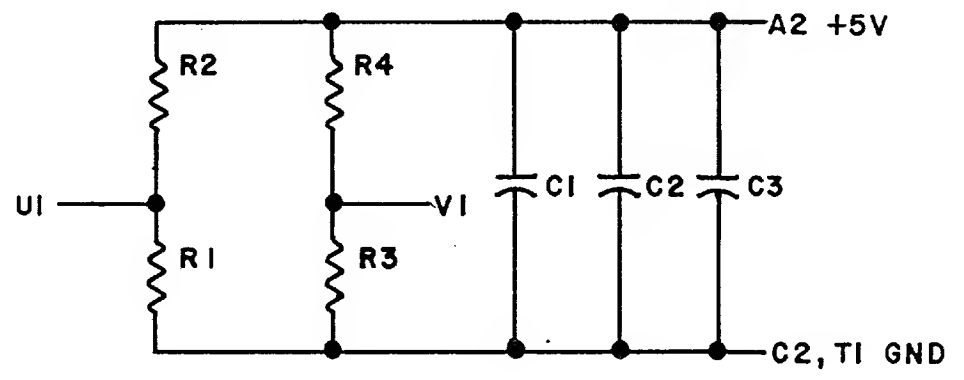


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NOTES:  
PIN 7 ON EACH IC = GND  
PIN 14 ON EACH IC = +5V



E1 THRU E3	INTEGRATED CKT. DEC7430N	1905578
R2 & R4	RES. 330 1/4W 10% CC	1300293
R1 & R3	RES. 750 1/4W 5% CC	1301401
C1 THRU C3	CAP. .01MFD 100V 20% DISC	1001610
PARTS LIST		A-PL-M119-0-0
REFERENCE DESIGNATION	DESCRIPTION	PART NO.

REVISIONS	CHK	CHG NO.	REV.
	6804	A	
	MM	00001	B

DRN.	DATE
M. Waller	8-1-67
CHK'D	DATE
R. Silberman	8/14/67
ENG.	DATE
Alan Rickett	8/11/67
PROD.	DATE

TRANSISTOR & DIODE CONVERSION CHART			
DEC	EIA	DEC	EIA

**digital**  
EQUIPMENT CORPORATION  
MAYNARD, MASSACHUSETTS

TITLE 3-8 INPUT NAND-GATES M119			
SIZE	CODE	NUMBER	REV.
B	CS	M119-0-1	B
PRINTED CIRCUIT REV.			